

	<b>L #</b>	<b>Hits</b>	<b>Search T xt</b>	<b>DB</b>
<b>1</b>	<b>L1</b>	<b>69</b>	<b>(361/734,738).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>2</b>	<b>L2</b>	<b>59</b>	<b>1 and (capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>3</b>	<b>L3</b>	<b>23</b>	<b>2 and ((power voltage) with (plane\$1 layer\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>4</b>	<b>L4</b>	<b>36</b>	<b>2 not 3</b>	<b>USP AT; US-P GPU B</b>
<b>5</b>	<b>L5</b>	<b>4</b>	<b>("4803595"   "5177594"   "5629838"   "5977640").PN.</b>	<b>USP AT</b>
<b>6</b>	<b>L6</b>	<b>120</b>	<b>361/306.2.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>7</b>	<b>L7</b>	<b>45</b>	<b>6 and ((power voltage reference) with (plane\$1 layer\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>8</b>	<b>L8</b>	<b>67</b>	<b>(6 not 7) and (capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hit</b>	<b>S arch T xt</b>	<b>DB</b>
<b>9</b>	<b>L9</b>	<b>366</b>	<b>(361/780,793,794).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>10</b>	<b>L10</b>	<b>186</b>	<b>9 and (capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>11</b>	<b>L11</b>	<b>157</b>	<b>10 and ((power voltage reference) with (plane\$1 layer\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>12</b>	<b>L12</b>	<b>240</b>	<b>361/782.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>13</b>	<b>L13</b>	<b>158</b>	<b>12 and (capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>14</b>	<b>L14</b>	<b>55</b>	<b>13 and ((power voltage reference) with (plane\$1 layer\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>15</b>	<b>L15</b>	<b>194</b>	<b>11 14</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>S arch T xt</b>	<b>DBs</b>
<b>16</b>	<b>L18</b>	<b>45</b>	<b>malladi\$.in.</b>	<b>USP AT; US-P GPU B</b>
<b>17</b>	<b>L19</b>	<b>8</b>	<b>18 and (capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>18</b>	<b>L20</b>	<b>842</b>	<b>257/777.ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>19</b>	<b>L21</b>	<b>195</b>	<b>20 and (capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>
<b>20</b>	<b>L22</b>	<b>72</b>	<b>21 and ((power voltage reference) with (plane\$1 layer\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>21</b>	<b>L23</b>	<b>2204</b>	<b>(257/691,700,724).ccls.</b>	<b>USP AT; US-P GPU B</b>
<b>22</b>	<b>L24</b>	<b>665</b>	<b>23 and (capacitor\$1 condenser\$1)</b>	<b>USP AT; US-P GPU B</b>

	<b>L #</b>	<b>Hits</b>	<b>S arch Text</b>	<b>DBs</b>
<b>23</b>	<b>L25</b>	<b>352</b>	<b>24 and ((power voltage reference) with (plane\$1 layer\$1))</b>	<b>USP AT; US-P GPU B</b>
<b>24</b>	<b>L26</b>	<b>289</b>	<b>25 not (2 7 8 15 22)</b>	<b>USP AT; US-P GPU B</b>